

PATENT

G0131




CERTIFICATE OF MAILING (37 CFR 1.8(a))

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Date:

4-1-05

  
Himanshu S. Amin

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of:

Inventor(s): Khoi A. Phan, et al.

Examiner: John S. Ruggles

Serial No: 10/050,417

Art Unit: 1756

Issue Date: July 6, 2004

Patent No: 6,759,179

Title: METHODS AND SYSTEMS FOR CONTROLLING RESIST RESIDUE  
DEFECTS AT GATE LAYER IN A SEMICONDUCTOR DEVICE  
MANUFACTURING PROCESS

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**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT**

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Dear Sir:

It is noted that errors appear in this patent of a typographical nature or character as more fully described below and it is believed such errors occurred in good faith and correction thereof does not involve such changes in the patent as would constitute new matter or would require reexamination. Accordingly, a Certificate of Correction is requested.

Attached hereto, in duplicate, is form PTO/SB/44 with at least one copy being suitable for printing.

The typographical errors, fully described on form PTO/SB/44, are believed to have been made by the Patent Office. Accordingly, no fee is believed to be necessary.

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Respectfully submitted,

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**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO. : 6,759,179  
DATED : July 6, 2004  
INVENTOR(S) : Khoi A. Phan, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In claim 2, line 5, please change "substate" to "substrate".

In claim 9, line 1, please change "spinning" to "rinsing".

In claim 10, line 1, please change "era" to "method".

In column 1, line 66, please change "modem" to "modern".

In column 4, line 39, please change "DLV" to "DUV".

In column 5, line 67, please change "fist" to "first".

**MAILING ADDRESS OF SENDER:**

AMIN & TUROCY, LLP  
24th Floor, National City Center  
1900 E. 9th Street  
Cleveland, Ohio 44114

**PATENT NO. 6,759,179**

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